



PATENT ABSTRACTS OF JAPAN

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PURPOSE: To take the synchronization timing between two kinds of system clocks in a digital equipment.

CONSTITUTION: Two kinds of system clocks having a frequency being a common measure are given to a digital equipment and in the synchronizing signal generator consisting mainly of an oscillator 3 whose input terminal 4 receives one basic system clock or a signal having a frequency f_0 being a greatest common measure as a synchronization input signal and whose oscillating frequency is set to a frequency f_2 of other system clock, since the stability of the synchronizing input signal decides the stability of the generator, the oscillator 3 and peripheral components are simplified and formed by inexpensive components, the cost is reduced and the space is saved.

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